

# NVIDIA® RTX™ 5000 Ada GENERATION

## SPECIFICATIONS

<b>Architecture</b>	NVIDIA Ada Lovelace Architecture
<b>Process Size</b>	4 nm NVIDIA Custom Process   TSMC
<b>Die Size</b>	608.4 mm
<b>Transistors</b>	76.3 Billion
<b>CUDA Parallel Processing Cores</b>	12,800
<b>Tensor Cores</b>	400
<b>RT Cores</b>	100
<b>Single-Precision Performance<sub>1</sub></b>	65.3 TFLOPS
<b>Tensor Performance<sub>1</sub></b>	1044.4 TFLOPS <sub>2</sub>
<b>RT Core Performance<sub>1</sub></b>	151.0 TFLOPS
<b>GPU Memory</b>	32 GB GDDR6 with ECC
<b>Memory Interface</b>	256-bit
<b>Memory Bandwidth</b>	576 GB/s
<b>Display Connectors</b>	DP 1.4a (4) <sup>3</sup>
<b>Graphics Bus</b>	PCI Express 4.0 x16
<b>Form Factor</b>	4.4" (H) x 10.5" (L) Single Slot
<b>Thermal Solution</b>	Blower Fan Active
<b>NVIDIA 3D Vision and 3D Vision Pro</b>	Support via 3-pin mini-DIN
<b>Frame Lock</b>	Compatible with NVIDIA Quadro Sync II
<b>Maximum Power Consumption</b>	250W
<b>NVENC   NVDEC</b>	2x   2x   Includes AV1 encode and Decode

<sup>1</sup> Peak rates are based on GPU boost clock.

<sup>2</sup> Effective FP8 TFLOPS using the new sparsity feature.

<sup>3</sup> Display ports are on by default for the RTX 5000 Ada Generation. Display ports are not active when using vGPU